## **Course: Advanced Analog IC Design**

**Lecture 3: Linearity Measurement** 

Prof. Sanjay Vidhyadharan



website: sanjayvidhyadharan.in



sanjayvidhyadharan.in Course: Advanced Analog IC Design

The network is linear and memoryless if out put is defined as :



and Non-Linear if out put is defined as :

 $y(t) \approx \alpha_0 + \alpha_1 x(t) + \alpha_2 x^2(t) + \alpha_3 x^3(t) + \alpha_4 x^4(t) + \dots$ 

## 1 dB Compression Point

Sanjayvidhyadharan.in Course: Advanced Analog IC Design



## **1 dB Compression Point**

Course: Advanced Analog IC Design



## **Third-order Intercept Point (IP3)**



**Sanjayvidhyadharan.in** Course: Advanced Analog IC Design

$$y(t) \approx \alpha_0 + \alpha_1 x(t) + \alpha_2 x^2(t) + \alpha_3 x^3(t) + \alpha_4 x^4(t) + \dots$$

They grow at a rate of 3 dB for every 1 dB of increase in input power level. The desired signal grows at a rate of 1 dB for each dB of input power.



Example f1= 10 K and f2=10.1K Third order hormonic 20K-10.1 K = 9.9K First order we set f2=10.1K



**Sanjayvidhyadharan.in** Course: Advanced Analog IC Design

Thank you sants