Testability of VLSI

Lecture 6A: Testability Measures

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Fault Simulation



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TESTABILITY MEASURES



We need an algorithm to find which are difficult fouls to detect using random sequence of Test vectors to apply Sensitization , Propagation and Justification

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Setting Difficulty levels



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TESTABILITY MEASURES

SCOAP Controllability and Observability

Sandia Controllability/Observability Analysis Program.

SCOAP consists of six numerical measures for each signal (l) in the circuit:

- 1. Combinational 0-controllability, CC0(l)
- 2. Combinational 1-controllability, CC1(l)
- 3. Combinational observability, CO(l)
- 4. Sequential 0-controllability, SC0(l)
- 5. Sequential 1-controllability, SC1(l)
- 6. Sequential observability, SO(1)

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1. Set the difficulty of controlling each *primary input* (PI) to 0 (called *CC*0) to the value 1 and the difficulty of controlling each PI to 1 (called *CC*1) to the value 1.

2. We progress through the circuit in a forward pass, in *level order*. For each logic gate that we traverse, we add 1 to the controllability. This accounts for the logic depth.

(i) If a logic gate output is produced by setting only one input to a controlling value, then:

output controllability = min (input controllabilities) + 1

(ii) If a logic gate output can only be produced by setting all inputs to a non-controlling value, then:

output controllability = \sum (input controllabilities) + 1

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SCOAP controllability calculation

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3. After all controllabilities are established, we compute observabilities in a reverse pass starting from primary outputs (POs) and moving backwards to the PIs. We first set the output observability difficulty (called CO) to 0, making no distinction between logic 0 and 1 in observabilities.

4. For a logic gate with an input signal that needs to be observed, the difficulty of observing that input equals the observability of the output plus the difficulty of setting all other inputs to non-controlling values, plus 1 to account for the logic depth.

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SCOAP observability calculation





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