

Analog IC Design : 2022-23 Lecture 12 Stability and Oscillators

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Single Pole Response



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Single Pole Response

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Two Pole Response



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Three Pole Response





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(a) a component at ω_1 is sensed at input, (b) the component returns to subtractor with a 180 ° phase shift, (c) the subtractor enhances the signal at node B.

A negative feedback system may become unstable if the forward amplifier introduces a phase shift of $-180 \circ$ at a finite frequency, ω_1 , and the loop transmission |KH| is equal to unity at that frequency

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Example-1

We wish to apply negative feedback with K = 1 around the three-stage amplifier shown in Fig. 12.67(a). Neglecting other capacitances and assuming identical stages, plot the frequency response of the circuit and determine the condition for stability. Assume $\lambda = 0$.



tenth of this frequency, it reaches -135^{0} at $\omega_{_{D}}$, and approaches -270^{0}

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Example-1

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Example-2

A common-source stage is placed in a unity-gain feedback loop as shown in Fig. Explain why this circuit does not oscillate.



Since the circuit contains only one pole, the phase shift cannot reach $180 \circ$ at any frequency. The circuit is thus stable.

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Frequency Compensation



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Frequency Compensation



Miller Compensation



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That is, we choose $R_{F1} \ge 2R_{F2}$.

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