



BITS Pilani

Hyderabad Campus

Department of Electrical Engineering



Digital Design

First Semester 2020-21

Tutorial : 08

Sequential Circuit Design

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1 . Design a Modulo 5 Counter

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2 . Design a Modulo 10 Counter

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3.

Problem: Design self-correcting MOD-6 counter in which all the unused states leads to state CBA=000

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4 . Presettable Counter:

In Presettable counter, user can set initial state of the counter, after initial state, counter can be made to count either UP or Down.

MOD-8 Presettable counter with initial state = 101 (5):

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5.

- Input: $x(t)$
- Output: $y(t)$
- State: $(A(t), B(t))$
- What is the Output Function?
- What is the Next State Function?

