

MPI Tutorial-5 8086 Data Transfer ALPs

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Problem-1

Write a program to move the contents of the memory location 0500H to register BX and to CX. Add immediate byte 05H to the data residing in memory location, whose address is computed using DS = 2000H and offset = 0600H. Store the result of the addition in 0700H. Assume that the data is located in the segment specified by the data segment register DS which contain 2000H.



Problem-1

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Imme	diate to Re diate to Re	gister/Mer gister	nory		1100 1011	01 w
		OPCODE FE	TCH CYCLE			
	T1	T2	Т3	T4		
CLK						T
A15-A8	20	High-Order Memo	y Address	Unspecified		N
AD7-AD0) 04 Low-Order Memory Address	}{Opcode	Read 5C }	Decodes Opcode		
ALE	ALE = 1	ALE = 0				
IO/M		10 / M = 0				
S1		S1 = 1				X
SO		S0 = 1				X
RD		RD = 0				
WR		WR = 1				

1000 1100 1011	10 dw 011 w w reg	mod reg r/m mod 000 r/m data	data data if w = 1	data if w = 1
	Instruction	Description	Clock Cycles	Number of Bytes
	MOV	Acc → Mem	10	3
		$Mem \rightarrow Acc$	10	3
		$\operatorname{Reg} \rightarrow \operatorname{Reg}$	2	2
		$Mem \rightarrow Reg$	8 + EA	2-4
		$\text{Reg} \rightarrow \text{Mem}$	9 + EA	2-4
		Immediate \rightarrow Reg	4	2-3
		Immediate \rightarrow Mem	10 + EA	3-6
		$\text{Reg} \rightarrow \text{SS}, \text{DS}, \text{ES}$	2	2
		Mem \rightarrow SS, DS, ES	8 + EA	2-4
		Seg Reg \rightarrow Reg	2	2
		Seg Reg \rightarrow Mem	9 + EA	2-4
	XCHG	$\operatorname{Reg} \leftarrow \rightarrow \operatorname{Acc}$	3	1
		$\operatorname{Reg} \leftarrow \rightarrow \operatorname{Mem}$	17 + EA	2-4
		$\operatorname{Reg} \leftarrow \rightarrow \operatorname{Reg}$	4	2
	XLAT		11	1

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Data Transfer MOV = Move

Register/Memory to/from Register

Problem-2A : Byte Transfer

2.A Write an ALP that transfers a block of 100 bytes of data. The source and destination memory blocks start at 8000 H and 9000 H memory locations respectively. The data segment register value is 3000H.

Solution:

2000 MOV AX, 3000H 2003 MOV DS, AX 2005 MOV SI, 8000 H 2008 MOV DI, 9000 H 200B MOV CL, 64 H 200D MOV AL, [SI] 200F MOV [DI], AL 2011 INC SI 2012 INC DI 2013 DEC CL 2014 JNZ 200D 2017 HLT

- : Move initial address of DS register into AX.
- : DS loaded with AX
- : Source address put into SI.
- : Destination address put into DI.
- : Count value for number of bytes put into CL register
- : Source byte moved into AL
- : AL byte moved into destination address
- : Increment source address
- : Increment destination address
- : Decrement CL count
- : Jump to 200D H until CL = 0

Problem-2B : Word Transfer

2B. Write an ALP that transfers a block of 100 bytes of data. The source and destination memory blocks start at 8000 H and 9000 H memory locations respectively. The data segment register value is 3000H.

Solution:

2000 MOV AX, 3000H **2003 MOV DS, AX** 2005 MOV SI, 8000 H 2008 MOV DI, 9000 H 200B MOV CL, 32 H 200E MOV AX, [SI] 2010 MOV [DI], AX **2012 INC SI 2013 INC SI 2014 INC DI** 2015 INC DI **2016 DEC CL** 2017 JNZ 200E 201A HLT

- : Move initial address of DS register into AX.
- : DS loaded with AX
- : Source address put into SI.
- : Destination address put into DI.
- : Count value for number of bytes put into CX register
- : Source byte moved into AX
- : AX byte moved into destination address
- : Increment source address
- : Increment source address
- : Increment destination address
- : Increment destination address
- : Decrement CX count
- : Jump to 200D H until CX = 0

Problem-2C : Using Loop

2C. Write an ALP that transfers a block of 100 bytes of data. The source and destination memory blocks start at 8000 H and 9000 H memory locations respectively. The data segment register value is 3000H.

Solution:

2000 MOV AX, 3000H 2003 MOV DS, AX 2005 MOV SI, 8000 H 2008 MOV DI, 9000 H 200B MOV CX, 0064 H 200E MOV AL, [SI] 2010 MOV [DI], AL 2012 INC SI 2013 INC DI 2014 LOOP 200E H 2017 HLT

- : Move initial address of DS register into AX.
- : DS loaded with AX
- : Source address put into SI.
- : Destination address put into DI.
- : Count value for number of bytes put into CX register
- : Source byte moved into L
- : AL byte moved into destination address
- : Increment source address
- : Increment destination address

Problem-2D : Using String

2D. Write an ALP that transfers a block of 100 bytes of data. The source and destination memory blocks start at 8000 H and 9000 H memory locations respectively. The data segment register value is 3000H.

Solution:

2000 MOV AX, 3000H 2003 MOV DS, AX 2005 MOV ES , AX 2007 MOV SI, 8000 H 200A MOV DI, 9000 H 200D MOV CX, 0064 H 2010 CLD 2011 REP MOVSB 2012 HLT

- : Move initial address of DS register into AX.
- : DS loaded with AX
- : ES loaded with AX
- : Source address put into SI.
- : Destination address put into DI.
- : Count value for number of bytes put into CX register

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Problem-2E : Using String

2D. Write an ALP that transfers a block of 100 bytes of data. The source and destination memory blocks start at 8000 H and 9000 H memory locations respectively. The data segment register value is 3000H.

Solution:

2000 MOV AX, 3000H 2003 MOV DS, AX 2005 MOV ES , AX 2007 MOV SI, 8000 H 200A MOV DI, 9000 H 200D MOV CX, 0032 H 2010 CLD 2011 REP MOVSW 2012 HLT

- : Move initial address of DS register into AX.
- : DS loaded with AX
- : ES loaded with AX
- : Source address put into SI.
- : Destination address put into DI.
- : Count value for number of bytes put into CX register

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Problem-3

3. Write ALP that saves the contents of 8086's lower byte flags in memory location having an offset 1212 H and then to reload the flags from the contents of the memory location having an offset 2121 H.

Solution: 0200 LAHF 0201 MOV [1212], A H

0205 MOV AH, [2121]

3311

0209 SAHF 020A HLT : Load AH from flags

: Move the contents of AH to memory locations pointed to by offset 1212 H

: Move the contents of memory locations pointed to offset 2121 H to AH

: Store AH into flags

: Stop.